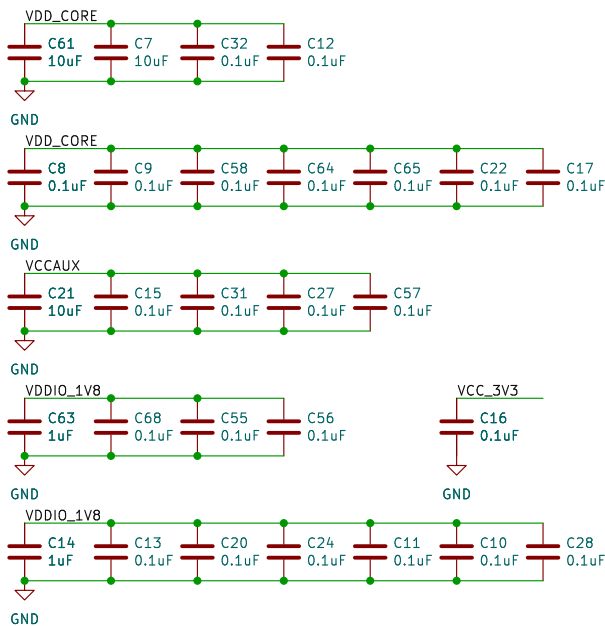
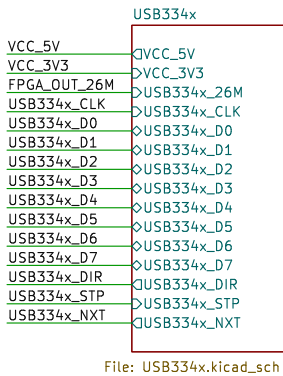
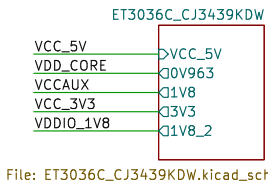
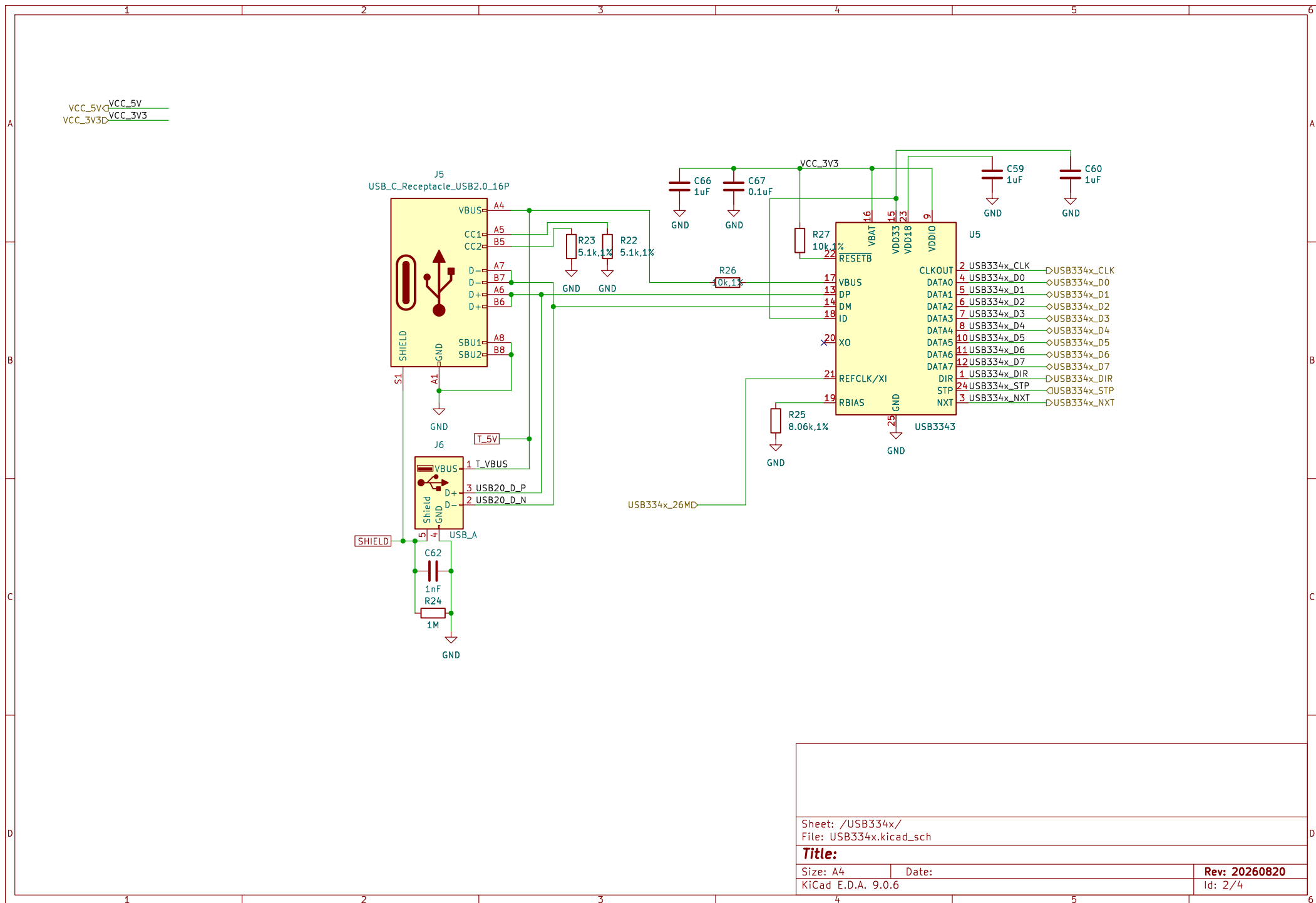
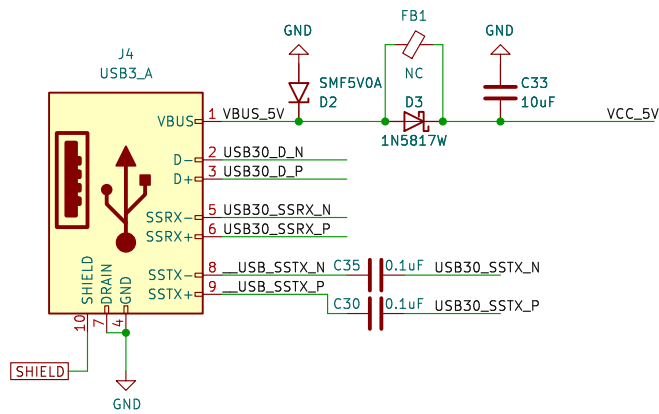
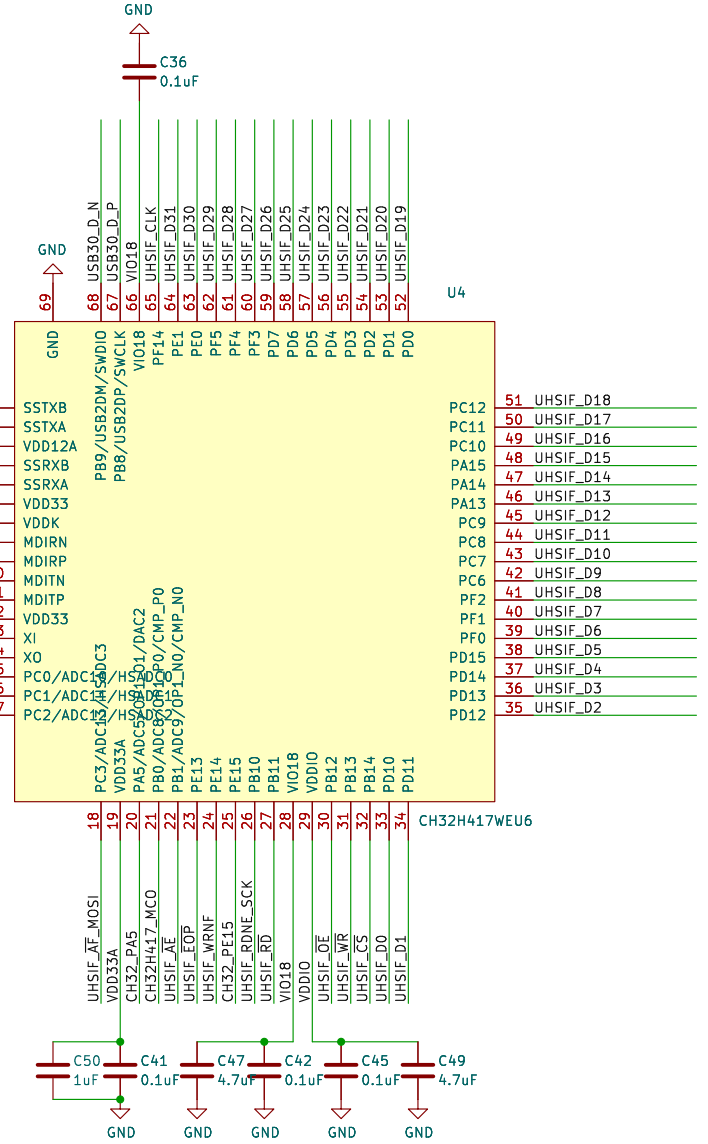
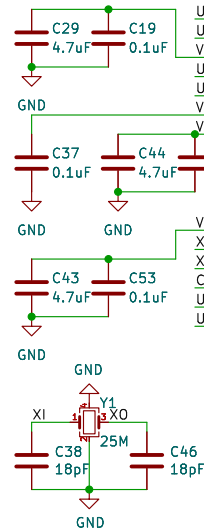






VBUS_5V <-> VBUS_5V
 VCC_5V <-> VCC_5V
 VCC_CH3V3 <-> VDD33
 VCC_CH3V3 <-> VDD33A
 VCC_CH1V8 <-> VIO18
 CH32_PC0 <-> CH32_PC0
 CH32_PA5 <-> CH32_PA5
 CH32_PE15 <-> CH32_PE15
 CH32H417_MCO <-> CH32H417_MCO
 UHSIF_SELO <-> UHSIF_SELO
 UHSIF_SELO <-> UHSIF_SELO
 UHSIF_SEL1_PC2 <-> UHSIF_SEL1_PC2
 UHSIF_AF_MOSI <-> UHSIF_AF_MOSI
 UHSIF_AE <-> UHSIF_AE
 UHSIF_AE <-> UHSIF_AE
 UHSIF_EOP <-> UHSIF_EOP
 UHSIF_WRN <-> UHSIF_WRN
 UHSIF_WRN <-> UHSIF_WRN
 UHSIF_RDNE_SCK <-> UHSIF_RDNE_SCK
 UHSIF_RD <-> UHSIF_RD
 UHSIF_RD <-> UHSIF_RD
 UHSIF_OE <-> UHSIF_OE
 UHSIF_OE <-> UHSIF_OE
 UHSIF_WR <-> UHSIF_WR
 UHSIF_WR <-> UHSIF_WR
 UHSIF_CS <-> UHSIF_CS
 UHSIF_CS <-> UHSIF_CS
 UHSIF_CLK <-> UHSIF_CLK
 UHSIF_CLK <-> UHSIF_CLK
 UHSIF_D[0..31] <-> UHSIF_D[0..31]

VDD33 VDDIO



USBSS:

SSRXA/SSRXB 交叉连接 TXN/TXP

SSTXA/SSTXB 交叉连接 TXN/TXP

[非必要]将 R32_LINK_CFG 寄存器的 RB_LINK_SS_PLR_SWAP 置为1

[非必要]代码: USBSSD->LINK_CFG |= LINK_SS_PLR_SWAP;

UHSIF:

UHSIF_PORT_RM = 01

UHSIF_CLK_RM = 10

Sheet: /ch32h417weu6/
File: ch32h417weu6.kicad_sch

Title:

Size: A4

Date:

KiCad E.D.A. 9.0.6

Rev: 20260820

Id: 4/4

